

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Kiran V. Chatty et al.

Application No.: 10/709,041

Confirmation No.: 3040

Filed: April 8, 2004

Art Unit: 2815

For: **LOW TRIGGER VOLTAGE ESD NMOSFET  
TRIPLE-WELL CMOS DEVICES**

Examiner: M. E. Warren

**AMENDMENT**

**MS AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed June 16, 2006, please amend the above-identified U.S. patent application as follows:

**AMENDMENTS TO THE CLAIMS** begin on page 2.

**REMARKS** begin on page 6.